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				Application Number	Unknown
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				Filing Date	September 18, 2003
				First Named Inventor	Katsumi ABE
				Art Unit	Unknown
Examiner Name	Unknown				
Attorney Docket Number	Q75817				
Sheet	1	of	1		

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
		Number	Kind Code <sup>1</sup> (if known)		
		US			
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FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Translation <sup>6</sup>
		Country Code <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)			
JP		JP	11-194320	A	07/21/1999		
JP		JP	11-194316	A	07/21/1999		

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city, and/or country where published.	Translation <sup>6</sup>
		T. Nakamura et al., "Low-Temperature Poly-Si TFT-LCD with Integrated Analog Circuit", Asia Display/IDW 01 Proceedings, October 16, 2001, pp. 1603-1606 with Abstract.	
		Y. Mikami et al., "A 5-in. SVGA TFT-LCD with Integrated Multiple DAC Using Low-Temperature poly-Si TFTs", Asia Display/IDW 01 Proceedings, October 16, 2001, pp. 1607-1610 with Abstract.	

Examiner Signature	<i>Tomyu</i>	Date Considered	May 30, 2006
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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